

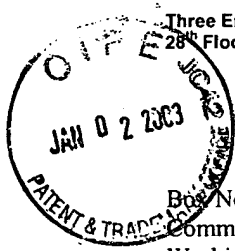
01-03-03

1765

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Docket No.: M-11706 US

January 2, 2003

By Non-Fee Amendment
Commissioner For Patents
Washington, D.C. 20231

Re: Applicant: Kirk Martin
 Assignee: Nisene Technology Group
 Title: Method And Apparatus For Etching A Semiconductor Die
 Serial No.: 09/902,931
 Examiner: Vinh, Lan
 Docket No.: M-11706 US

Filed: July 10, 2001
 Group Art Unit: 1765

Dear Sir:

Transmitted herewith are the following documents in the above-identified application:

- (1) Return Receipt Postcard;
- (2) This Transmittal Letter (1 p); and
- (3) Response to Office Action (1 p.)

- ☒ No additional fee is required.
☐ The fee has been calculated as shown below:

CLAIMS AS AMENDED

	Claims Remaining <u>After Amendment</u>		Highest No. Previously <u>Paid For</u>		Present <u>Extra</u>	<u>Rate</u>	Additional <u>Fee</u>	
Total Claims	26	Minus	34	=	0	x \$18.00	\$ 0.00	
Independent Claims	3	Minus	5	=	0	x \$84.00	\$ 0.00	
<input type="checkbox"/>	Fee of _____ for the first filing of one or more multiple dependent claims per application						\$	
<input type="checkbox"/>	Fee for Request for Extension of Time (three months)						\$	00
<u>Total additional fee for this Amendment:</u>							\$	<u>00</u>
<input checked="" type="checkbox"/>	Conditional Petition for Extension of Time: If an extension of time is required for timely filing of the enclosed document(s) after all papers filed with this transmittal have been considered, an extension of time is hereby requested.							
<input type="checkbox"/>	Please charge our Deposit Account No. 19-2386 in the amount of						\$	<u>00</u>
<input checked="" type="checkbox"/>	Also, charge any additional fees required and credit any overpayment to our Deposit Account No. 19-2386.							
Total:							\$	00

EXPRESS MAIL LABEL NO.:

EV 212 983 050 US

Respectfully submitted,

Robert D. Wasson
 Reg. No. 40,218

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#5
JAN
1/6/03

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 09/902,931 Confirmation No.: 9250
First Named Inventor: Kirk Martin Filing Date: July 10, 2001
Group Art Unit: 1765 Examiner: Vinh, Lan
Atty. Docket No.: M-11706 US
Title: Method And Apparatus For Etching A Semiconductor Die
Assignee: Nisene Technology Group

San Francisco, California
January 2, 2003

BOX NON-FEE AMENDMENT
COMMISSIONER FOR PATENTS
Washington, D. C. 20231

RESPONSE TO OFFICE ACTION

Dear Sir:

This is in response to a non-final Office Action dated December 11, 2002.

Applicant elects to have the inventions of Group I, claims 1-21 and 29-33, examined.

Applicant reserves the right to pursue the non-elected claims by a divisional application or otherwise.

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EXPRESS MAIL LABEL NO:

EV 212 983 050 US

Respectfully submitted,

Robert D. Wasson
Attorney for Applicant
Reg. No. 40,218

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